

SY DIPLOMA - DTE (CO / AIML / EXTC)

IMP QUESTIONS

Sr.No.	Topic	No. of times appeared in last 4 question paper
1	Number system conversion	6
2	universal gate, derive basic gates using NAND / NOR	5
3	Binary to gray conversion	4
4	K-MAP	4
5	Full adder	4
6	Memory	4
7	PLA	4
8	De-Morgan's law	4
9	Propagation delay, Power Dissipation, Fan-out, Basic gate	3
10	BCD adder using IC 7483	3
11	16:1 MUX using 4:1 MUX	3
12	2's complement subtraction	3
13	logic gates with their types	3
15	R-2R DAC (numerical)	3
16	BCD addition & binary addition	2
17	D flip flop	2
18	SAR ADC	2
19	SOP & POS example & minterm & maxterm	2
20	3bit/4bit/mod 10 asynchronous counter	2
21	SISO	2
22	BCD to 7 segment	2
23	boolean eq. using basic gates & simplification	2
24	Radix	2
25	T flip flop	1
26	compare sync. & async. counter	1
27	PAL	1
28	SR flip-flop	1
29	compare R-2R and weighted resistor method	1

30	Preset & clear	1
31	JK flip-flop	1
32	Sipo	1
33	Ring counter	1
34	Half adder	1
35	Master and slave JK flip-flop	1

